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ACADEMIC QUALIFICATIONS:

Master of Technology in Digital Electronics and Communication Systems.
Bachelor of Engineering in Electronics and Communication Engineering

ACADEMIC REWARDS / ACHIEVEMENTS: NIL

PROFESSIONAL EXPERIENCE:

S. No.	Designation	Institution Name	Working Period	
			From	To
1	Associate Professor	Gudlavalleru Engineering college, Gudlavalleru	Aug'2008	Till date
2	Assistant Professor	Gudlavalleru Engineering college, Gudlavalleru	June,2004	July,2008
3	Assistant Professor	Dr. Paul Raj Engineering college,Bhadrachalam.	Aug'2002	May, 2004

PROJECTS GUIDED:

PhD	Post Graduation	Under Graduation
NIL	12	21

SPECIALIZATION: Digital Electronics and Communication Systems

RESEARCH PAPERS / BOOKS:

1. Co-author for chapter 3.1 titled “Digital Communication Lab” of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016
2. Co-author for chapter 4.1 titled “Digital Communication Lab” of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
3. Co-author for chapter 5 titled “Analog Communication” of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
4. Co-author for chapter 5.2 titled “Microwave & Optical Communication Lab” of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015.

Papers Published in Journals No. : 11

1. “Universal steganalysis towards higher detection accuracy”, International Journal for Electronics Engineering, Vol. 10, Issue 02, ISSN: 0973-7383 ,June2018- December 2018, pp.258-263.
2. “Universal Image Steganalysis using Feature Extraction through the Combinational Transforms”, International Journal for Modern Trends in Science and Technology, Vol. 03, Issue 07, ISSN: 2455-3778 ,July2017, pp.306-309.
3. “Hiding of Data in Images using Spread Spectrum Technique”, IJCSITS, ISSN: 2249 – 9555, Page No.11-14, Volume 6, Issue 4, and August 2016.
4. “A Robust Image Steganography Embed and Extraction by Using Combinational Transform Technique in painting”, IJARCSSE, ISSN: 2277 – 128X, Page No.329-333, Volume 5, Issue 12, and December 2015.
5. “Improved seamless cloning image in painting”, IOSR-JVSP, e-ISSN: 2319 – 4197, Page No.38-45, Volume 4, Issue 5, and September 2014.
6. “Implementation of Digital Communication Laboratory on FPGA”, IJARCCCE, ISSN: 2278 – 1021, Page No.4318-4322, Volume 2, Issue 11, and November 2013.
7. “Raspberry Pi for Commercial Applications”, IJCT, ISSN: 2277 – 3061 Volume 11, Issue 2, Page No.2250-2255, and October 2013.
8. “Design and Implementation of Complex Floating point processor using FPGA”, VLSICS, Vol.4, No.5, Page No.53-61, October 2013.

9. "An Efficient Reversible Design of BCD Adder", IJCTA, ISSN: 2229-6093, Vol.No.3, ISSUE:5, Page No.1699-1703,2012.
- 10."Color Image Contrast Enhancement Using $L^*a^*b^*$ Color space", IJEECS, ISBN: 0974-2166, Vol.No.9, ISSUE: 2, 2012.
- 11."Disclosure of Hidden Messages Using Classifier Based on Stastical Moments of Wavelet Characteristic Function", IJECE, ISBN: 0974-2166, Vol.No.4, Page No.243-250, July-December 2011.

Papers Published in Conferences No. :4

1. "Efficient FPGA Implementation of AES Algorithm Well Suited for Embedded Applications", International Conference ICECE, Page No.289-292, 1st-2nd October 2011.
2. "Steganography-Security Through Images", Second International Conference RSPS, Page No.452-455,7-9th January 2010.
3. "Reconstruction of Signal with UnderSampling-Compressed Sampling", Second International Conference RSPS, Page No.532-535,7-9th January 2010.
4. "Design of Generalized Half Band Filters using Elliptic Approximation", National Conference FACE, Page No.30-33,19-21st March 2009.

Workshops /Conferences /Seminars Attended No. : 09

1. Two day seminar on "Advancements in mobile communications" organized by Dept. of ECE,University college of engineering & technology, ANU, Guntur, during 24th-25th February, 2016.
2. One day workshop on "Research in Energy Management Wireless Sensor Networks &Smart Transportation Systems ", organized by Institute of Engineers(India) ,Vijayawada local center, on 29th March , 2015.
3. One day seminar on "Research in Signal and Image processing on FPGA's and Embedded Platforms" organized by Institute of Engineers(India) ,Vijayawada local center, on 22nd November , 2014.
4. Three day workshop on "Theory&Applications of intelligent signal processing", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during on 28th Febraury 2014 to 2nd March 2014.
5. Two week ISTE workshop on "Signals & Systems" conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January2014 (This workshop was held under the national mission on education through ICT (MHRD)).
6. Two day workshop on "Issues in Integrated Circuit Design", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 13th - 14th December ,2013.

7. Two week ISTE workshop on “Analog Electronics” conducted by Indian Institute of Technology Kharagpur from 4th to 14th June 2013 (This workshop was held under the national mission on education through ICT (MHRD)).
8. Two day ISTE workshop on “Aakash for Education” conducted by Indian Institute of Technology, Bombay from 10th to 11th November 2012 (This workshop was held under the national mission on education through ICT (MHRD)).
9. Two day workshop on “Microwave Engineering Concepts and Lab Practice”, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 9th - 10th June, 2012.

Online Certifications/Training Programs Attended No. :09

1. A National Level Webinar on “Intellectual Property Rights & Patents - A View” organized by IQAC, Gudlavalleru Engineering College, Gudlavalleru held during 10th-12th August 2020.
2. AICTE sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" (Decentralized Network Synchronization) Organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, during 10th-15th August 2020.
3. Online National Level FDP on "Cyber Security" Organized by Department of Computer Science & Engineering and IT in association with CyberSpy, Gudlavalleru Engineering College, Gudlavalleru during 22nd-26th July 2020.
4. One week online Certificate Course on "Digital Teaching Techniques" Organized by ICT Academy during 29th-04th July 2020.
5. One week online Knowledge Development program on "Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 8th-13th June 2020.
6. One day webinar online training program on "Impact of COVID in Engineering Education & Way forward" Organized by Indian Society for Technical Education (ISTE) Chapter, Gudlavalleru Engineering College, Gudlavalleru on 09th June 2020.
7. Two week online Faculty Development program on "Internet Of Things (IoT)" Organized by Andhra Pradesh State Skill Development Corporation (APSSDC) during 18th-30th May 2020.
8. One day webinar online training program on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 25th May 2020.
9. One day webinar online training program on "Speech Signal Processing" Organized by Department of Department of Computer Science and Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram on 17th May 2020.

Certifications/Training Programs Attended No. :20

1. Three day training program on "Advanced Development Tools Based IoT" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 11th-13th November 2019.
2. One week short term training program through ICT mode on "Evaluating Students' Performance and Designing Question Papers" Organized by National Institute of Technical Teachers' Training and Research, Kolkata in Gudlavalleru Engineering College, Gudlavalleru during 25th February-01st March 2019.
3. One week Faculty Development program on "Machine Learning Applications in Signal Processing" Organized by the E&ICT Academy, National Institute of Technology, Warangal at Gudlavalleru Engineering College, Gudlavalleru during 04th – 09th February, 2019.
4. Two day training program on " Design and Development of Digital Circuits using VLSI Tools" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th December 2018.
5. One week Faculty Development program on "Communications and Signal Processing" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October-03rd November 2018.
6. One week national level Faculty Development program on "Signal and Image Processing using Labview" Organized by Signal Processing Group of Department of Electronics and Communication Engineering in association with NI Systems Pvt. Ltd. during 13th-18th November 2017.
7. Two day faculty training program on "Advances in VLSI Design and Embedded Systems" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th November 2017.
8. Two day staff training program on "MATLAB TOOLS FOR ENGINEERS-HANDS ON EXPERIENCE" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 10th-11th January 2017.
9. One week Faculty Development program on "Hands on Experience in Signal Processing applications using NI Multisim" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 7th-12th November 2016.
10. Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications using NI Multisim" Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 4th-5th November 2016.
11. Three day national level Faculty Development Program on "Emdedded Systems", organized by SAMVID,ECE Association, Organized by Department of Electronics and Communication Engineering, Chalapathi Institute of Engineering and Technology, Guntur during 3rd- 5th Octoberber,2016.

12. A one week Faculty Development Program on “Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on Experience”, Organized by Communication Research group in association with Vertex soft,Hyderabad ,Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 1st- 5th December,2015.
13. One week refresher course on “Microprocessors &Microcontrollers”, Organized by R&D Center, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 17th- 21st November, 2015.
14. Two day training program on “Digital Image Processing using MATLAB” Organized by Computer Centre, MIT Campus, Anna University, Chennai during 20th-21st June ,2015.
15. One week refresher course on “Electronic Devices and Circuits”, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 23rd- 28th September ,2013.
16. Two day training program on “MATLAB, SIMULINK & Related Tool Boxes for Engineering Education” Organized by Math Works at Gudlavalleru Engineering College 3rd-4th April ,2013.
17. Two day Faculty Development Program on “VLSI and Embedded System Design”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 7th- 8th July,2012.
18. One week refresher course on “Pulse& Digital Circuits”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 21st- 26th November 2011.
19. Five day training program on “Mentor Graphics (Front End and Back end) and Xilinx Tools” Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 13th- 17th September 2010.
20. Two week staff Development Program on “Current Trends in Biometrics”, Organized by Department of Information Technology, Gudlavalleru Engineering College, Gudlavalleru during 2nd- 14th November 2009.

CONSULTANCY ACTIVITIES: 01